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	2 An application specific Java processor with reconfigurabiliti Kimura, S.; Kida, H.; Takagi, K.; Abematsu, T.; Watanabe, K.; Design Automation Conference, 2000. Proceedings of the ASP-DAC 2 South Pacific, 25-28 Jan. 2000 Pages:25 - 26  [Abstract] [PDF Full-Text (260 KB)] IEEE CNF	
	Frequency dispersion in partially depleted SOI MOSFET output Fung, S.K.H.; Man Kit Wong; Chan, M.; Nguyen, C.T.; Ko, P.K.; SOI Conference, 1996. Proceedings., 1996 IEEE International, 30 Sept. 1996 Pages: 146 - 147	

[PDF Full-Text (172 KB)] [Abstract]

### 4 Ground fault monitoring in Alcator C-MOD

Flanary, D.;

Fusion Engineering, 1993., 15th IEEE/NPSS Symposium on , Volume: 2 , 11-Oct. 1993

Pages:885 - 888 vol.2

[Abstract] [PDF Full-Text (372 KB)] IEEE CNF

### 5 An ASIC engine compiler for digital signal processing

Payne, J.G.; Kruse, S.M.; Smith, S.G.; Morgan, R.W.; CompEuro '91. 'Advanced Computer Technology, Reliable Systems and Applications'. 5th Annual European Computer Conference. Proceedings., 13-: May 1991

Pages: 792 - 795

[Abstract] [PDF Full-Text (284 KB)] IEEE CNF

### 6 An ASIC DSP engine compiler: philosophy and applications

Payne, J.G.; Kruse, S.M.; Smith, S.G.; Morgan, R.W.; Acoustics, Speech, and Signal Processing, 1991. ICASSP-91., 1991 Internatio Conference on , 14-17 April 1991 Pages:1225 - 1228 vol.2

[Abstract] [PDF Full-Text (512 KB)] IEEE CNF

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Member Services	Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE , Volume
O- Join IEEE O- Establish IEEE Web Account	1 , 27 Nov1 Dec. 2000 Pages:152 - 156 vol.1  [Abstract] [PDF Full-Text (372 KB)] IEEE CNF
O- Access the IEEE Member Digital Library	17 High performance analog-to-digital converters (ADCs) for signal processing

Jha, A.R.;

Microwave and Millimeter Wave Technology, 2000, 2nd International Conference. ICMMT 2000, 14-16 Sept. 2000

Pages:32 - 35

[Abstract] [PDF Full-Text (200 KB)] IEEE CNF

18 Simulated faults on directional, ground, overcurrent relays with emphasis on the operational impact on mutually coupled, intact lines Homeyer, J.W.; Etezadi-Amoli, M., Sr.;

Power Engineering Society Winter Meeting, 2000. IEEE , Volume: 3 , 23-27 Ja 2000

Pages:1911 - 1916 vol.3

[Abstract] [PDF Full-Text (392 KB)] IEEE CNF

#### 19 Electrical characterization of BGA packages

Mattei, C.; Agrawal, A.P.;

Electronic Components and Technology Conference, 1997. Proceedings., 47th

21 May 1997

Pages:1087 - 1093

[Abstract] [PDF Full-Text (808 KB)] IEEE CNF

# 20 Hot-carrier induced degradation of critical paths modeled by rule-k analysis

Kuusinen, S.B.; Chenming Hu;

Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995 May 1995

Pages:69 - 72

[Abstract] [PDF Full-Text (276 KB)] IEEE CNF

# 21 Predicting circuit performance using circuit-level statistical timing analysis

Brawhear, R.B.; Menezes, N.; Oh, C.; Pillage, L.T.; Mercer, M.R.; European Design and Test Conference, 1994. EDAC, The European Conference Design Automation. ETC European Test Conference. EUROASIC, The European Event in ASIC Design, Proceedings., 28 Feb.-3 March 1994 Pages: 332 - 337

[Abstract] [PDF Full-Text (472 (8)] IEEE CNF

### 22 General test framework for straight-line detection by Hough transf

Hare, A.R.; Sandler, M.B.;

Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993

Pages:239 - 242 vol.1

[Abstract] [PDF Full-Text (328 KB)] IEEE CNF

#### 23 Neural networks for digital adder

Morisue, M.; Sakai, K.; Iizuka, T.;

Circuits and Systems, 1991., IEEE International Sympoisum on , 11-14 June

Pages: 1605 - 1608 vol.3

[Abstract] [PDF Full-Text (208 KB)] IEEE CNF

# 24 TATOO: an industrial timing analyzer with false path elimination ar test pattern generation

Benkoski, J.; Stewart, R.B.;

Design Automation. EDAC. Proceedings of the European Conference on , 25-2 Feb. 1991

Pages:256 - 260

[Abstract] [PDF Full-Text (324 KB)] IEEE CNF

## 25 Hierarchical timing view generation including accurate modeling for false paths

Das, P.; Johannes, P.; Claesen, L.; De Man, H.;

Custom Integrated Circuits Conference, 1989., Proceedings of the IEEE 1989 18 May 1989

Pages:13.3/1 - 13.3/4

[Abstract] [PDF Full-Text (240 KB)] IEEE CNF

## 26 Importance sampling simulation techniques applied to estimating alarm probabilities

Lu, D.; Yao, K.;

Circuits and Systems, 1989., IEEE International Symposium on , 8-11 May 19 Pages: 598 - 601 vol.1

[Abstract] [PDF Full-Text (268 K8)] IEEE CNF

### 27 False operation of vector surge relays

Freitas, W.; Wilsun Xu;

Power Delivery, IEEE Transactions on , Volume: 19 , Issue: 1 , Jan. 2004

Pages:436 - 438

[Abstract] [PDF Full-Text (128 KB)] IEEE JNL

## 28 Pyramidal motion estimation techniques exploiting intra-level moticorrelation

Zan, J.; Ahmad, M.O.; Swamy, M.N.S.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactic [see also Circuits and Systems II: Express Briefs, IEEE Transactions on], Vol

50 , Issue: 2 , Feb. 2003

Pages:83 - 93

[Abstract] [PDF Full-Text (920 K8)] IEEE JNL

# 29 **EMI effects and timing** design for increased reliability in digital sys Chappel, J.F.; Zaky, S.G.;

Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti on [see also Circuits and Systems I: Regular Papers, IEEE Transactions

on], Volume: 44, Issue: 2, Feb. 1997

Pages:130 - 142

[Abstract] [PDF Full-Text (608 KB)] IEEE JNL

### 30 Using polynomic embedding for neural network design

Porter, W.A.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactic [see also Circuits and Systems II: Express Briefs, IEEE Transactions on], Vol 39, Issue: 6, June 1992

Pages:369 - 376

[Abstract] [PDF Full-Text (612 KF)] IEEE JNL

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[Abstract] [PDF Full-Text (1064 KB)] IEEE JNL

a Impact of solder pad size on solder joint reliability in flip chip PRG4

2 Impact of solder pad size on solder joint reliability in flip chip PBGA packages

Mercado, L.L.; Sarihan, V.; Guo, Y.; Mawer, A.;

Advanced Packaging, IEEE Transactions on [see also Components, Packaging Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on], Volume: 23, Issue: 3, Aug. 2000

Pages:415 - 420

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Digital Library

Access the

[Abstract] [PDF Full-Text (292 KB)] IEEE JN

3 Modeling and optimization of thin-film devices with  $Si_{1-x}Ge_x$  alloys Koschier, L.M.; Wenham, S.R.; Green, M.A.; Electron Devices, IEEE Transactions on , Volume: 46 , Issue: 10 , Oct. 1999 Pages: 2111 - 2115

[Abstract] [PDF Full-Text (164 KB)] IEEE JNL

4 A continuous and general model for boron diffusion during post-impannealing including damaged and amorphizing conditions

Baccus, B.; Vandenbossche, E.;

Semiconductor Manufacturing, IEEE Transactions on , Volume: 9 , Issue: 1 , |

1996

Pages:59 - 66

[Abstract] [PDF Full-Text (784 KB)] IEEE JNL

#### 5 Dynamic time step control algorithm enhancements

Bach, J.C.;

Behavioral Modeling and Simulation, 2000. Proceedings. 2000 IEEE/ACM International Workshop on , 19-20 Oct. 2000

Pages:83 - 88

[Abstract] [PDf Full-Text (392 K8)] IEEE CNF

# 6 Impact of solder pad size on solder joint reliability in flip chip PBGA packages

Mercado, L.; Sarihan, V.; Yifan Guo; Mawer, A.;

Electronic Components and Technology Conference, 1999. 1999 Proceedings.

49th , 1-4 June 1999

Pages: 255 - 259

[Abstract]

[PDF Full-Text (308 KB)]

# 7 Immediate transmission scheme of s-ALOHA with PCT (power contr during transmission) method

**IEEE CNF** 

Ishida, A.; Yamamoto, M.; Okada, H.; Mutsuura, K.; Tezuka, Y.; Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993

Pages: 2228 - 2231 vol.4

[Abstract] [PDF Full-Text (256 KB)] IEEE CNF

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